



Docket No.: GR 97 P 6457 P

2811
B

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

By: [Signature] Date: October 6, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/595,860 Confirmation No: 2756
Applicant : Jörg Berthold, et al.
Filed : June 16, 2000
Art Unit : 2811
Examiner : Cuong Q. Nguyen
Title : Integrated Electrical Circuit and Method for Fabricating It
Docket No. : GR 97 P 6457 P
Customer No. : 24131

CLAIM FOR PRIORITY

Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

Claim is hereby made for a right of priority under Title 35, U.S. Code, Section 119, based upon the German Patent Application 197 55 869.0, filed December 16, 1997.

A certified copy of the above-mentioned foreign patent application is being submitted herewith.

Respectfully submitted,

[Signature]
Kerry P. Sisselman
Reg. No. 37,237

Date: October 6, 2005
Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

/av

BUNDESREPUBLIK DEUTSCHLAND

CERTIFIED COPY OF
PRIORITY DOCUMENT



09,595,860

Prioritätsbescheinigung über die Einreichung einer Patentanmeldung

BEST AVAILABLE COPY

Aktenzeichen: 197 55 869.0

Anmeldetag: 16. Dezember 1997

Anmelder/Inhaber: Siemens Aktiengesellschaft, 80333 München/DE

Bezeichnung: Integrierte elektrische Schaltung und Verfahren zu ihrer Herstellung

IPC: H 01 L 23/522

Die Akte dieser Patentanmeldung ist ohne vorherige Offenlegung vernichtet worden.

München, den 20. September 2005
Deutsches Patent- und Markenamt

Der Präsident
Im Auftrag

Letang